

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	1	"609109"	US-PGPUB	OR	OFF	2005/01/18 11:18
S2	3	eidson-mark-e.in.	US-PGPUB	OR	OFF	2005/01/18 11:19
S3	1	eidson-mark-e.in.	USPAT	OR	OFF	2005/01/18 11:19
S4	34	bios near4 pci adj bus	USPAT	OR	OFF	2005/01/18 11:26
S5	6	bios near4 (peripheral mezzanine) adj2 bus	USPAT	OR	OFF	2005/01/18 11:28
S6	56	rom near4 pci adj bus	USPAT	OR	OFF	2005/01/18 11:26
S7	738	"boot rom"	USPAT	OR	OFF	2005/01/18 11:26
S8	1731	"system rom"	USPAT	OR	OFF	2005/01/18 11:26
S9	2	S8 near4 pci adj bus	USPAT	OR	OFF	2005/01/18 11:27
S10	2	S8 near8 pci adj bus	USPAT	OR	OFF	2005/01/18 11:27
S11	4	S7 near8 pci adj bus	USPAT	OR	OFF	2005/01/18 11:27
S12	4	(S7 S8) near8 (peripheral mezzanine) adj2 bus	USPAT	OR	OFF	2005/01/18 11:29
S13	548	access\$3 near2 bios	USPAT	OR	OFF	2005/01/18 12:05
S14	107	S13 with address	USPAT	OR	OFF	2005/01/18 11:53
S15	5	S14.clm.	USPAT	OR	OFF	2005/01/18 11:30
S16	13	S13 with pci	USPAT	OR	OFF	2005/01/18 12:12
S17	3	S13 with mask\$3	USPAT	OR	OFF	2005/01/18 12:13
S18	46	bios near6 mask\$3	USPAT	OR	OFF	2005/01/18 12:26
S19	5370	a20\$5	USPAT	OR	OFF	2005/01/18 12:27
S20	71	bios same S19	USPAT	OR	OFF	2005/01/18 12:29
S21	0	S20.ab.	USPAT	OR	OFF	2005/01/18 12:29
S22	0	S20.clm.	USPAT	OR	OFF	2005/01/18 12:29
S23	8	bios same S19 with mask\$3	USPAT	OR	OFF	2005/01/18 12:30
S24	1196	(boot bootable) adj (device rom)	USPAT	OR	OFF	2005/01/18 12:31
S25	20	S24 near6 mask\$3	USPAT	OR	OFF	2005/01/18 12:32
S26	77	S24 near3 address	USPAT	OR	OFF	2005/01/18 12:34
S27	4	S26.clm.	USPAT	OR	OFF	2005/01/18 12:32
S28	33	S24 with pci	USPAT	OR	OFF	2005/01/18 12:34
S29	4	S26 with pci	USPAT	OR	OFF	2005/01/18 12:36
S30	31	rom with S19	USPAT	OR	OFF	2005/01/18 12:38
S31	2	S24 same S19	USPAT	OR	OFF	2005/01/18 12:38
S32	75	boot adj2 vector	USPAT	OR	OFF	2005/08/23 11:53
S33	387	boot near2 address	USPAT	OR	OFF	2005/08/23 12:31
S34	43	S33.clm.	USPAT	OR	OFF	2005/08/23 12:09
S35	24	S33 with bus	USPAT	OR	OFF	2005/08/23 12:25
S36	5	("6212651").URPN.	USPAT	OR	OFF	2005/08/23 12:24
S37	20	S33 with decod\$3	USPAT	OR	OFF	2005/08/23 12:25
S38	7123	rom near2 address	USPAT	OR	OFF	2005/08/23 12:31
S39	940	S38 with bus	USPAT	OR	OFF	2005/08/23 12:37
S40	56	S39.clm.	USPAT	OR	OFF	2005/08/23 12:33
S41	20	S39 same pci	USPAT	OR	OFF	2005/08/23 12:37
S42	0	S39 same eisa	USPAT	OR	OFF	2005/08/23 12:37
S43	1044	S38 with decod\$3	USPAT	OR	OFF	2005/08/23 12:37
S44	8	S43 same pci	USPAT	OR	OFF	2005/08/23 12:41
S45	56	S43.clm.	USPAT	OR	OFF	2005/08/23 13:22
S47	1	ignor\$3 near3 S33	USPAT	OR	OFF	2005/08/23 15:17

S48	1	ignor\$3 with S33	USPAT	OR	OFF	2005/08/23 15:20
S49	14	ignor\$3 with S38	USPAT	OR	OFF	2005/08/23 15:18
S50	8	substitut\$3 near4 S38	USPAT	OR	OFF	2005/08/23 15:19
S51	3	substitut\$3 with S33	USPAT	OR	OFF	2005/08/23 15:19
S52	81	ignor\$3 with bios	USPAT	OR	OFF	2005/08/23 15:22
S53	1098	ignor\$3 near2 address	USPAT	OR	OFF	2005/08/23 15:22
S54	10	S53 same pci	USPAT	OR	OFF	2005/08/23 15:25
S55	42	S53 with (chipset controller)	USPAT	OR	OFF	2005/08/23 15:26
S56	1397	bypass\$3 near3 (chipset controller)	USPAT	OR	OFF	2005/08/23 15:27
S57	29	S56 with address	USPAT	OR	OFF	2005/08/23 15:28
S58	6	S56 with mask\$3.	USPAT	OR	OFF	2005/08/23 15:29
S59	6	a20 same (S53 S56)	USPAT	OR	OFF	2005/08/23 15:29